LISTING OF THE CLAIMS

The following listing of claims replaces all prior versions.

1. (Previously Presented) A method for processing an analog video signal that represents a work for which rights information is asserted, the signal including

a rights assertion mark (RAM) in the video portion of the signal within the visible picture such that capture of the picture will include the mark, and

copy control information (CCI) bits in the vertical blanking interval of the signal for representing copying and redistribution rules;

comprising the steps of:

determining if the RAM and CCI bits are present in the work,

if the RAM is present and the CCI bits are present, then applying the copying and redistribution rules represented by the CCI bits, and

if the RAM is present but the CCI bits are not, then applying the most restrictive copying and redistribution rules that can be represented by the CCI bits;

wherein the CCI bits are represented as CGMS-A information, the most restrictive copying rule is "copy never", and the most restrictive redistribution rule is "no redistribution".

2. & 3. Cancelled

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4. (Previously Presented) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 7 wherein the video signal further includes secondary copy control information (SCCI) that conforms with the CCI bits, and if the RAM and SCCI bits are present but the CCI bits are not, then the copying and redistribution rules represented by the SCCI bits are applied.

5. (Original) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 4 wherein the SCCI bits are in the form of a watermark in the audio portion of the analog video signal.

6. (Original) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 4 wherein the SCCI bits are in the form of a digital signature in the vertical blanking interval of the analog video signal, where the digital signature is a function of the CCI bits and selected pixels of the visible picture of the work.

7. (Original) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 1 wherein the RAM is represented by VEIL modulation of the video signal

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8. (Original) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 1 wherein the video signal further includes secondary copy control information (SCCI) that conforms with the CCI bits, and if the RAM and SCCI bits are present but the CCI bits are not, then applying the copying and redistribution rules represented by the SCCI bits.

9. (Original) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 8 wherein the SCCI bits are in the form of a watermark in the audio portion of the work.

10. (Original) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 8 wherein the SCCI bits are in the form of a digital signature in the vertical blanking interval of the analog video signal, where the digital signature is a function of the CCI bits and selected pixels of the visible picture of the work.

11. (Previously Presented) A method for asserting and identifying rights information in a work represented by an analog video signal, comprising the steps of:

inserting a rights assertion mark (RAM) in the video portion of the signal within the visible picture such that capture of the picture will include the mark, and

inserting copy control information (CCI) bits in the vertical blanking interval of the signal for representing copying and redistribution rules , wherein the CCI

bits are represented as CGMS-A information, the most restrictive copying rule is "copy never", and the most restrictive redistribution rule is "no redistribution.",

where the signal is to be processed by a device that operates on it as follows:

(i) if the RAM is present and the CCI bits are present, then the copying and redistribution rules represented by the CCI bits are applied, and

(ii) if the RAM is present but the CCI bits are not, then the most restrictive copying and redistribution rules that can be represented by the CCI bits are applied.

12. (Cancelled)

13. (Original) A method for asserting and identifying rights information in a work represented by an analog video signal in accordance with claim 11 wherein the RAM is represented by VEIL modulation of the video signal.

14. (Original) A method for asserting and identifying rights information in a work represented by an analog video signal in accordance with claim 13 further including the step of inserting in the signal secondary copy control information (SCCI) that conforms with the CCI bits.

15. (Original) A method for asserting and identifying rights information in a work represented by an analog video signal in accordance with claim 14 wherein the SCCI bits are in the form of a watermark in the audio portion of the work.

16. (Original) A method for asserting and identifying rights information in a work represented by an analog video signal in accordance with claim 14 wherein the SCCI bits are in the form of a digital signature in the vertical blanking interval of the analog video signal, where the digital signature is a function of the CCI bits and selected pixels of the visible portion of the work.

17. (Currently Amended) A method for processing an analog video signal that represents a work for which rights information is asserted, the signal including

a rights assertion mark (RAM) in presented by a Veil modulation of the video portion of the signal within the visible picture such that capture of the picture will include the mark, and

copy control information (CCI) bits for representing copying rules, said CCI bits being represented as CGMS-A information in the vertical blanking interval of the analog signal;

comprising the steps of:

determining if the RAM and CCI bits are present in the work,

if the RAM is present and the CCI bits are present, then applying the copying rules represented by the CCI bits, and

if the RAM is present but the CCI bits are not, then applying default copying rules;

wherein the video signal further includes secondary copy control information (SCCI) that conforms with the CCI bits, and if the RAM and SCCI bits are present but the CCI bits are not, then the copying and redistribution rules represented by the SCCI bits are applied.

18. (Cancelled)

19. (Cancelled)

20. (Cancelled)

21. (Currently Amended) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 20 17 wherein the SCCI bits are in the form of a watermark in the audio portion of the work.

22. (Currently Amended) A method for processing an analog video signal that represents a work for which rights information is asserted in accordance with claim 20 17 wherein the SCCI bits are in the form of a digital signature in the vertical blanking

interval of the analog video signal, where the digital signature is a function of the CCI

bits and selected pixels of the visible picture portion of the work.

23. (Cancelled)

24. (Cancelled)

25. (Cancelled)

26. (Cancelled)

27. (Cancelled)

28. (Cancelled)

29. (Cancelled)

30. (Cancelled)

31. (Cancelled)

32. (Cancelled)

33. (Currently Amended) A method of controlling the processing of an analog video signal in a player device using Copy Control Information (CCI) rules, wherein said analog video signal includes CCI bits and a rights assertion mark (RAM) confirming the presence of CCI bits in the analog video signal, comprising the steps of:

checking said signal for said CCI bits and said RAM;

if both are present then operating said device in accordance with said CCI bits; and

if said RAM is present but said CCI bits are absent then operating said device in a default mode; and

if said CCI bits are absent, said device is operated in a mode in which said analog video signal cannot be redistributed.

34. (Previously Presented) The method of claim 33 wherein said CCI bits represent CGMS-A information.

35. (Previously Presented) The method of claim 33 wherein said video signal includes vertical blanking periods and said CCI bits are imbedded in said vertical blanking periods.

36. (Previously Presented) The method of claim 33 wherein said RAM is implemented using VEIL modulation of said analog video signal.

37. (Previously Presented) The method of claim 33 wherein in the absence of said CCI bits, said device is operated in a mode in which said analog video signal cannot be recorded.

38. (Cancelled)